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**Complete if Known**

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|------------------------|-----------------------|
| Application Number     | 10/777,368            |
| Filing Date            | February 12, 2004     |
| First Named Inventor   | Andrew J. Ritz        |
| Art Unit               | 2186                  |
| Examiner Name          | Unknown               |
| Attorney Docket Number | MS306248.1/MSFTP553US |

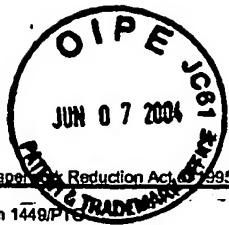
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| Examiner<br>Signature | /Chun-Kuan Lee/ | Date<br>Considered | 04/21/2006 |
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PTO/SB/088 (08-03)

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INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT

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Sheet

2

of

2

Application Number

10/777,368

Filing Date

February 12, 2004

First Named Inventor

Andrew J. Ritz

Art Unit

2186

Examiner Name

Unknown

Attorney Docket Number

MS306248.1/MSFTP553US

## NON PATENT LITERATURE DOCUMENTS

| Examiner Initials* | Cite No. <sup>1</sup> | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T <sup>2</sup> |
|--------------------|-----------------------|---|----------------|
| CKL                |                       | M. TRAMS, W. REHM, D. BALKANSKI, S. SIMEONOV. Memory Management in a combined VIA/SCI Hardware. In proceedings to PC-NOW 2000, International Workshop on Personal Computer based Networks of Workstations held in conjunction with the IPDPS, 2000. 12 pages.   |                |
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| CKL                |                       | Gamma, E., et al. Design Patterns: Elements of Reusable Object-Oriented Software. Addison-Wesley, Boston, 1995.   |                |
| CKL                |                       | PCI Express Base Specification Revision 1.0a.<br><a href="http://www.pcisig.com/specifications/pciexpress/base/">http://www.pcisig.com/specifications/pciexpress/base/</a>  |                |
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Examiner  
Signature

/Chun-Kuan Lee/

Date

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